



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग

DEPARTMENT OF ELECTRONICS ENGINEERING

सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: डीईसीई/पीएच.डी./ प्रवेश / 807 / २०२५-२६

दिनांक: १६/०७/२०२५

Ph. D. Admission Phase-III July -2025

Autumn Semester (Academic Year: 2025-26)

Selected Candidates (Department of Electronics Engineering)

Sr. No.	App Id	Name of Candidates	Supervisor	Category
1.	SVNIT3P5JP	AVINASHSINGH CHAUHAN	Dr. P. J. Engineer (Supervisor) Dr. Shilpi Gupta (Co-supervisor)	ERS (Part time)
2.	SVNIT3PAMX	KUTE SAGAR LAXMAN	Prof. Piyush Patel (Supervisor) Dr. Vivek Garg (Co-supervisor)	ERS (Part time)

NOTES:

1. All selected candidates shall complete the registration formalities and make the fee payment as per the schedule given on the Institute website.
2. The admissions are governed by the academic rules and regulations of the programme.

शिल्पी गुप्ता
16/07/25

Dr. Shilpi Gupta
Head, DoECE

आचार्यप्रतिभा
21/07/25
Dean (Academic)